

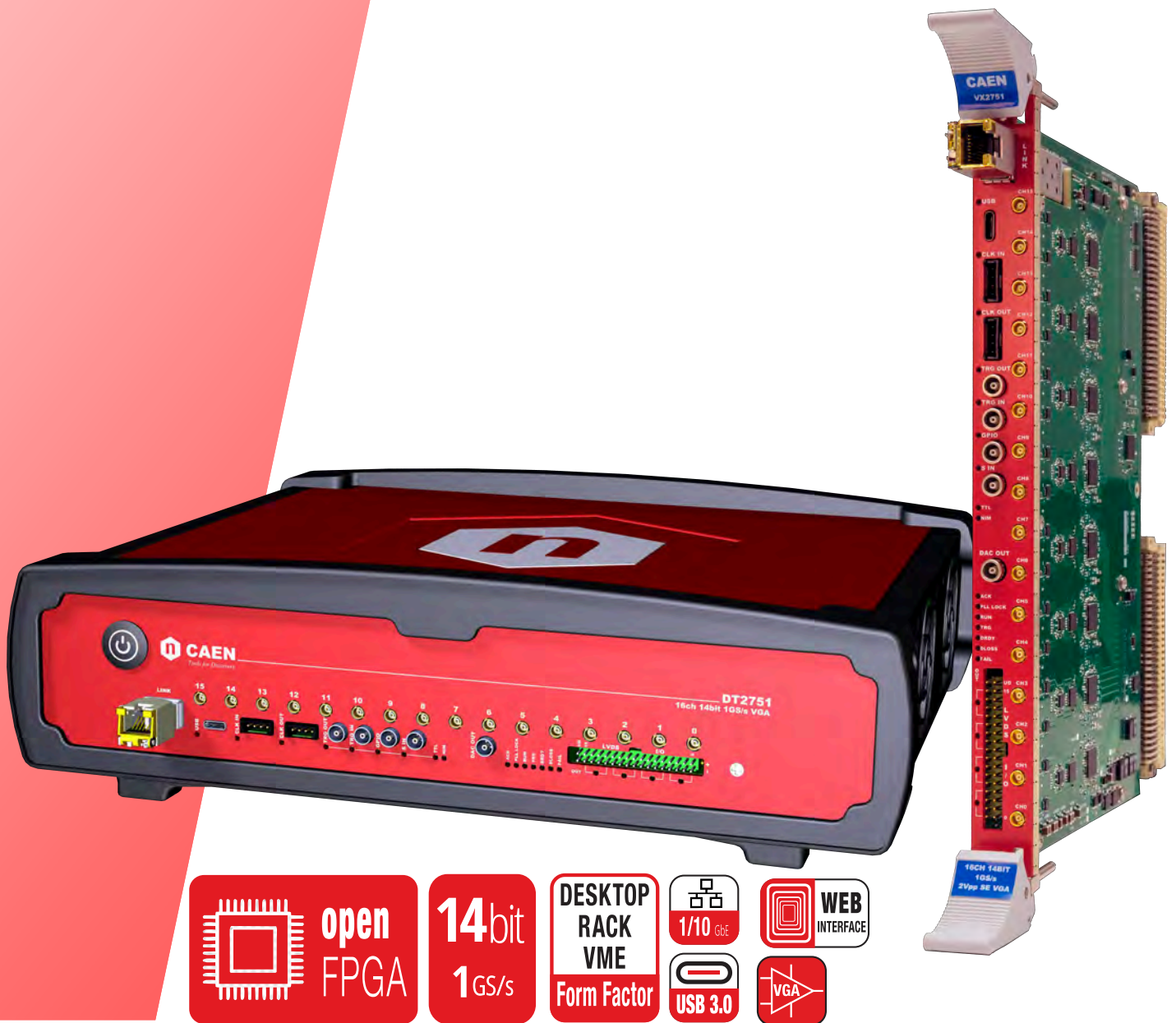
PRELIMINARY



2751

Digitizer Family

16-Channel 14-bit 1 GS/s



High-density 1GHz digitizer – ultra-fast, ultra-precise, ultra-compact!

Overview

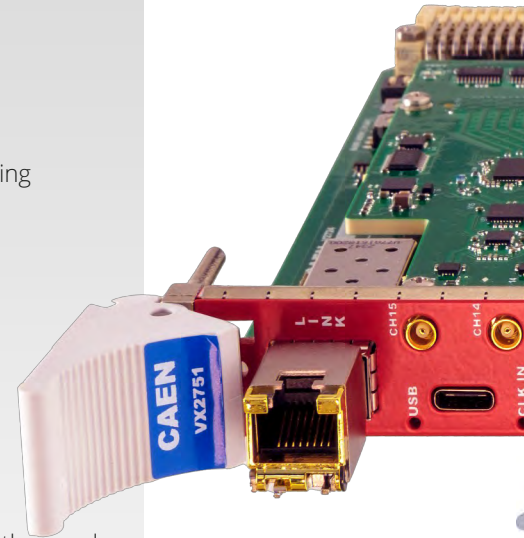
The CAEN 2751 Digitizer Family is a high-performance solution designed for a wide range of applications in Radiation Detection, such as Nuclear and Particle Physics, High Timing Resolution, Fast Neutron Spectroscopy, Dark Matter and Astroparticle, Fusion Plasma diagnostic, and Homeland Security. Featuring **16 independent channels with 14-bit ADCs at 1 GS/s**, it is optimized to manage fast signals coming from organic, inorganic and liquid scintillators coupled to PMTs or SiPMs, Diamond detectors and others. The onboard **Xilinx Zynq UltraScale+** Multiprocessor System-on-Chip allows for operating a wide selection of CAEN firmware, User firmware developed on the Open-FPGA blocks, User software routines running on the Open-Arm Linux. Available in **Desktop** (DT2751) and **VME64X** (VX2751) form factors, they offer a versatile platform for both laboratory and large-scale experimental setups.

Highlights

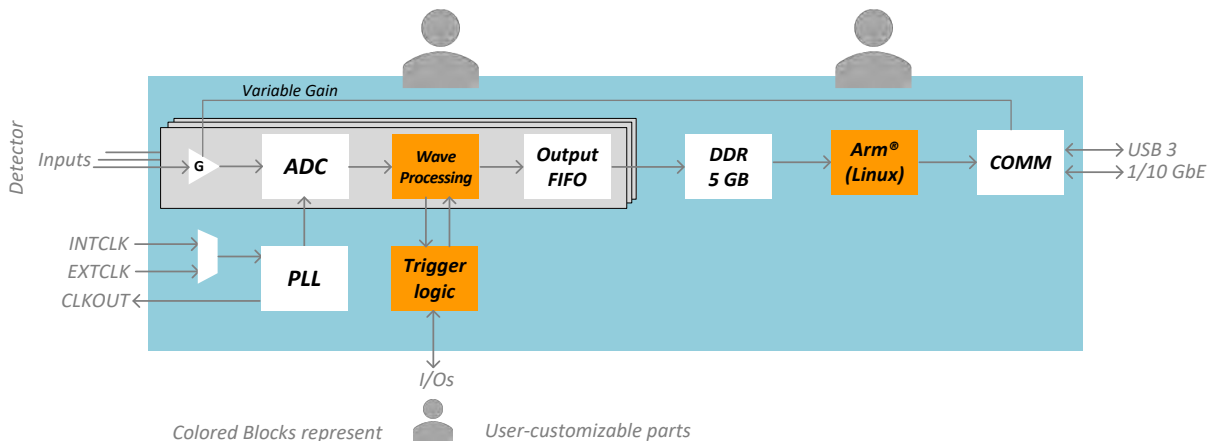
- Up to **16** independent detector channels sampled by **1GS/s 14-bit** ADCs with individual DC offset adjustment
- Software selectable analog gain (VGA) independently on each channel
- Available in different form factors: **VME64X** and **Desktop**
- VME64X version fully compliant with CAEN **μ-crate** module
- Front panel readout via **USB-3.0** or **1/10Gb** Ethernet
- Up to **84 ms** of continuous wave recording per channel in **DDR4** memories
- Onboard **Zynq® UltraScale+** MPSoC integrating an **Arm®**-based CPU running Linux®
- **Open FPGA** architecture fully supported by **Sci-Compiler** tool
- CAEN firmware solutions ready to use for Charge Integration (PSD) and Waveform Recording
- Digital **CFD** function for sub-ns timing measurements
- Triggered and streaming readout modes supported
- Multi-board synchronization facilitated by front panel programmable digital I/Os
- Front panel DAC output programmable for signal inspection, pulse generation, majority level

Software

- GUI-based readout software available for multiparametric spectroscopy (**CoPASS**) or waveform recording (**WaveDump2**)
- Firmware/software generator and compiler for the Open FPGA (**Sci-Compiler**), eliminating the need for FPGA programming skills.
- Libraries (**FELib**) and demo codes are provided for software customization



2751 DIGITIZER ARCHITECTURE



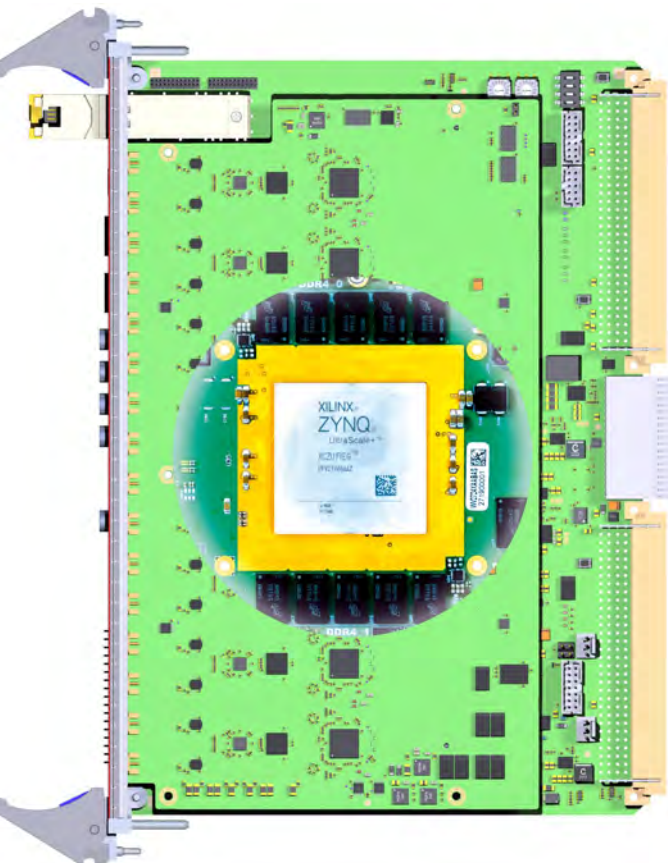
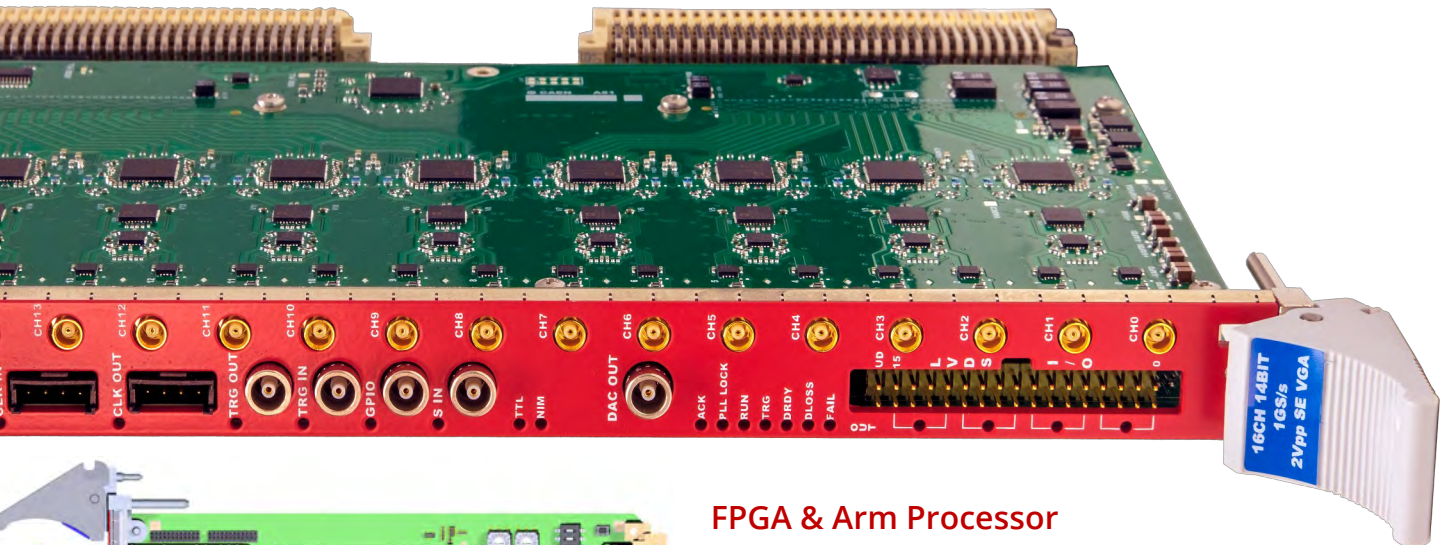
Data Acquisition

CAEN digitizers support multiple data acquisition modes, implemented as distinct FPGA firmware solutions, making them adaptable to a wide range of radiation detection applications in Nuclear Physics. The CAEN 2751 Digitizer Family features **16** single-ended channels, each one capable of digitizing detector signals with a **14-bit ADC** at **1 GS/s**. The data acquisition is driven by the trigger signal generation and the identification of a Region of Interest (ROI), defined in terms of samples or time duration. Trigger sources can basically be either local (channel self-trigger), or external, or software. Once acquired, the digitized data is processed in the FPGA, stored in high-speed memory by events including Trigger ID/ Timestamp tag, and then transferred via high-bandwidth communication interfaces for further analysis. The Digitizer operates in different acquisition modes, designed to balance throughput, latency, and data efficiency based on experimental needs.

Triggered mode: All channels acquire data simultaneously upon a global trigger generated by a Central Logic Unit that processes the local triggers received from the channels. Optionally, the external and software trigger can also be the source for the global trigger. Zero suppression algorithms can be applied to remove non-significant data and reduce the readout payload.

Streaming readout mode: Each channel identifies the ROI autonomously. The channel self-trigger is used to acquire data independently on the other channels. This mode implements automatic zero suppression (not fired channels are not acquired), it maximizes acquisition rates, and it is ideal for applications requiring real-time parameter extraction.

Correlated: Coincidence/anticoincidence logic can be configured to validate the event acquisition upon the combination of the local trigger with other local triggers and/or external I/Os



FPGA & Arm Processor

The CAEN 2751 Digitizer Family has been equipped with a Xilinx Zynq UltraScale+ MSoC including FPGA and Arm-based processor running Linux.

In the 2751 architecture, specific functional FPGA blocks are open for custom firmware solutions. Users can control the data output information and implement complex trigger logics using I/O signals to validate or discard the events.

Users can also automatize processes programming custom software routines in the Linux operating system hosted on the embedded Arm processor.

Synchronization

Multi-board synchronization of 2751 digitizers can be achieved by front panel cabling optimization (e.g. both clock and sync signals sent on CLK-OUT / CLK-IN), or by P0 backplane(*).

(* Customization available on request

Connectivity

Multi-interface options are available on the front panel: **USB-3.0** with maximum tested transfer rate of 280 MB/s; **1/10 Gb Ethernet** with up to **850 MB/s** measured transfer rate for 10G UDP.

Firmware & Software

The 2751 Digitizer Family can be operated upon both pre-configured firmware developed by CAEN and custom user-generated firmware, offering flexibility for a wide range of applications. More than a firmware can be simultaneously stored in the Digitizer FLASH memory and quickly activated if necessary.

CAEN provides ready-to-use firmware solutions, optimized for specific acquisition and processing needs:

Scope firmware based on full waveform recording in triggered acquisition mode. Zero suppression function is available to reduce unnecessary data readout.

DPP-PSD firmware implements Digital Pulse Processing algorithms of charge integration and pulse shape discrimination. Physical parameters like energy, timestamp and PSD are extracted from the waveforms acquired in streaming readout mode. CFD function is available for fine timestamp.

Dedicated CAEN software tools are available to manage the firmware solutions:

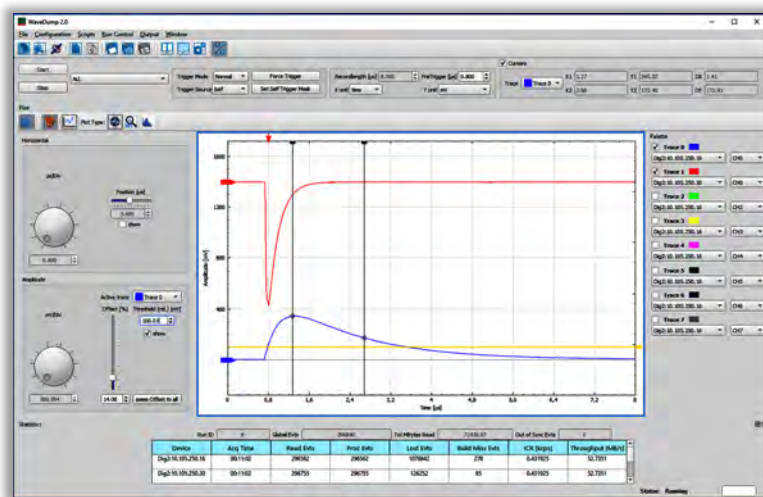
WaveDump2 works with Scope Firmware, providing a user-friendly interface for waveform acquisition and real-time data visualization.

CoMPASS is compatible with DPP-PSD Firmware, allowing multiparametric data acquisition, real-time PSD analysis, and event-based visualization.

FELib (Front-End library) facilitates users needing custom software development. It is a comprehensive C/C++ API for direct interaction with the digitizer hardware, enabling custom DAQ implementations with full control over data acquisition parameters and processing workflows.

For users requiring custom acquisition and processing, the Open FPGA architecture enables firmware customization through **Sci-Compiler**. This versatile graphical tool not only makes users capable of producing personalized firmware solutions without need of FPGA programming language skills but also generates the drivers and libraries and offers graphical tools to develop custom DAQ software.

Additional **DPP firmware solutions** may be developed by CAEN in the future, extending the processing capabilities of the 2751 series.



www.caen.it/products/wavedump2/

WaveDump2 software

- Waveform recording application
- Multi-board management
- Simultaneous plot of waveforms from up to 8 input channels
- Flexible and easy configuration of channel and trigger settings
- Runtime FFT analysis

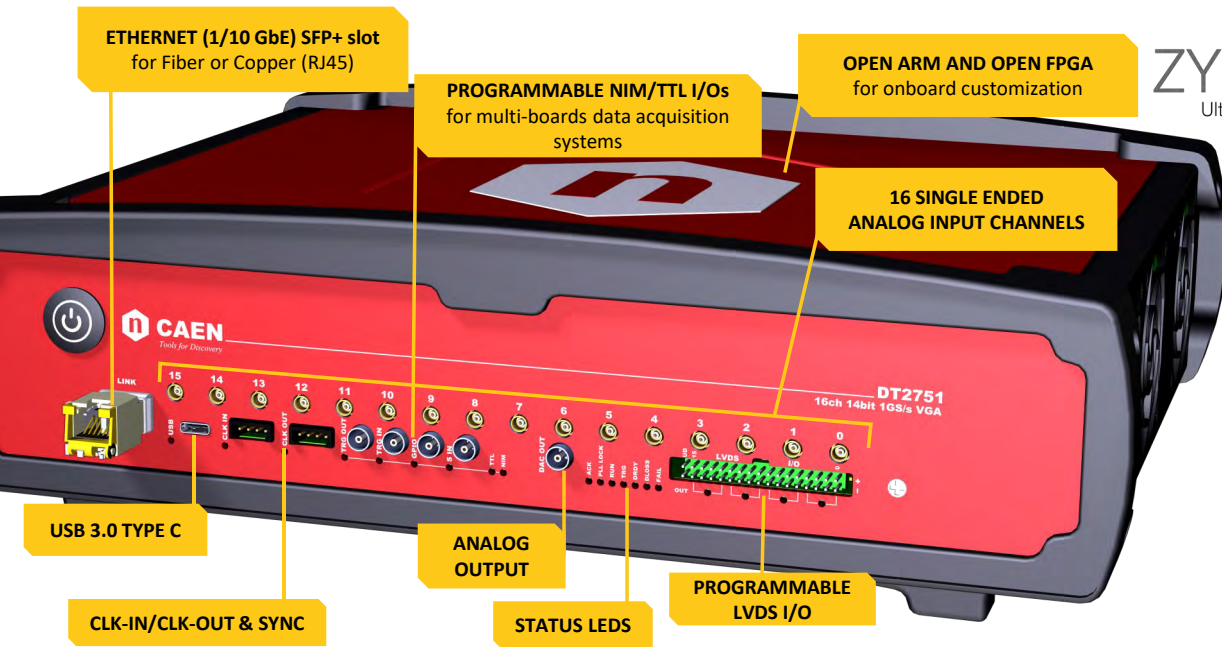
WaveDump2 is a C++ software designed to support the Digitizer Series 2.0, running the scope firmware for waveform recording provided by CAEN. Developed using the **Qt cross-platform** application development framework, this software offers an advanced and user-friendly configuration GUI. It provides a comprehensive range of tools and functionalities to manage various hardware parameters, from basic settings to more specific ones. Configuration settings can be conveniently stored in or loaded from configuration files. The software also facilitates data acquisition from multiple boards and synchronized systems through a dedicated toolbar. The collected data can be saved in ASCII or binary file formats for offline analysis.

The program includes a plot section that functions as an 8-channel digital oscilloscope emulator. This tool allows users to review acquired waveforms, fine-tune device settings, and troubleshoot potential issues. The oscilloscope interface features cursors for on-screen measurements, as well as marker lines to indicate the trigger position and trigger threshold level. Traces can be individually enabled or disabled, and a legend is provided to easily identify displayed signals. The graphical tool offers zooming controls in both the vertical and horizontal directions. Basic processing capabilities, such as FFT (Fast Fourier Transform) and samples histogramming, are available in real-time.

WaveDump2 is compatible for Windows® and Linux® platforms.



www.caen.it/products/compass/

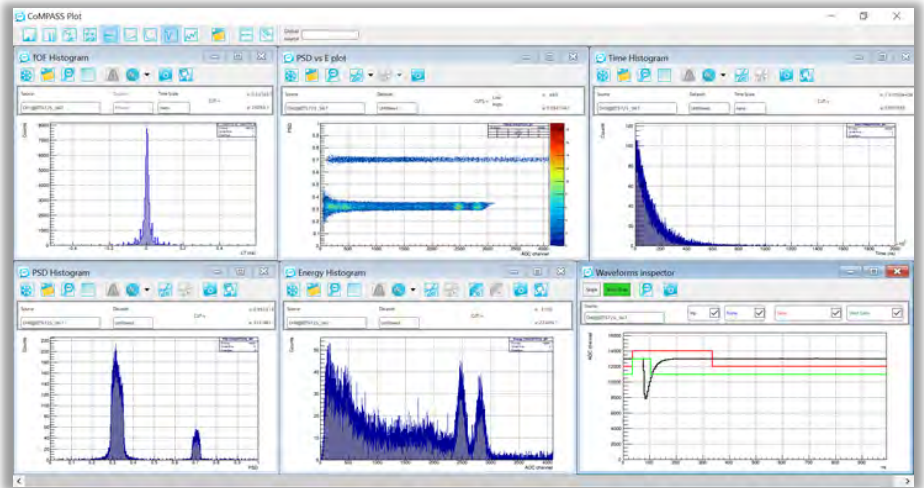


www.caen.it/products/compass/



CoPASS multiparametric software

- Simultaneous plot of waveforms, Time, Energy, PSD, and TOF spectra
- Online filtering (Time, Energy, PSD)
- Multi-board management
- Advanced data saving
- ROOT format data saving



CAEN Multi-Parameter Spectroscopy Software (**CoPASS**) is the data acquisition (DAQ) software designed for both Digitizer Series 1.0 and 2.0, running the DPP (Digital Pulse Processing) firmware provided by CAEN. It enables Multiparametric Data Acquisition for Physics Applications, where detectors can be directly connected to the digitizers/MCAs inputs, allowing simultaneous acquisition of energy, timing, and PSD (Pulse Shape Discrimination) spectra.

CoPASS software offers a user-friendly interface designed to manage the acquisition process using all the CAEN DPP algorithms. It provides easy configuration of acquisition parameters and allows up to six different plots and histograms to be displayed simultaneously. CoPASS can handle multiple boards and enables straightforward synchronization of multiboard systems. Some of the key features of CoPASS include event correlation between different channels (both in hardware and software), energy, PSD, and time selections, calculation and display of acquisition statistics (trigger rates, data throughput, percentage of discarded events due to selections, etc.), basic mathematical analysis of recorded spectra (ROI selection, background subtraction, peak fitting, etc.), saving output data files (raw data, lists, waveforms, spectra), and offline processing using saved files with different processing parameters.

For users familiar with the ROOT Analysis Framework, CoPASS provides the option to save output files (lists, waveforms, and spectra) in the ROOT TTree format, allowing easy post-processing with customized analysis code .

CoPASS is available for Windows[®] and Linux[®] platforms.  



www.caen.it/products/caen-toolbox/

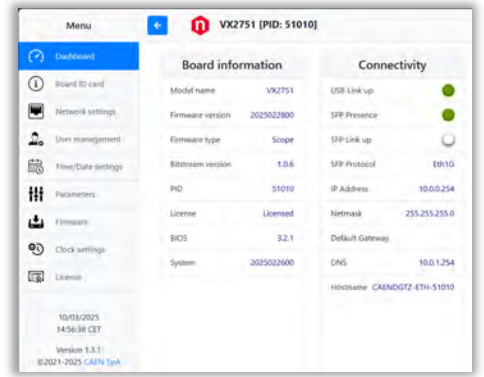
CAEN ToolBOX is a software utility designed to upgrade and manage the firmware on the 2751 Digitizer Family. This highly versatile software suite supports a wide selection of CAEN devices, including front-end boards, Digitizer 1.0 and 2.0 series, bridges and controllers, PLU, and power supplies. It offers a user-friendly GUI or command line application to manage onboard firmware, perform debug tests, and execute other useful functions. .



ToolBOX is available for Windows[®] and Linux[®] platforms.



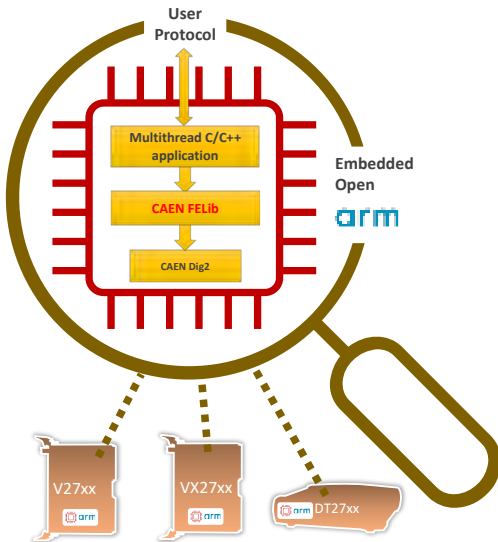
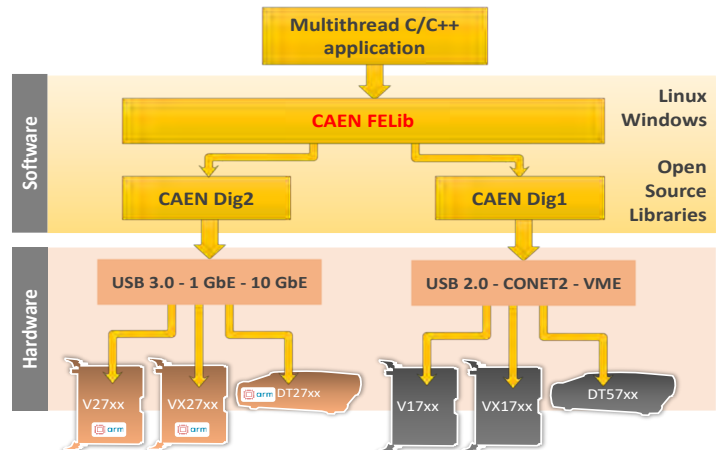
The Digitizer 2751 features an intuitive **Web Interface** that allows users to access key service functionalities directly from a web browser, without requiring additional software. This interface enables real-time hardware status monitoring, providing visibility on operating parameters and error notifications. Users can configure and manage network connectivity settings, either by manually assigning an IP address or using DHCP for automatic allocation. The Web Interface also supports firmware management, allowing seamless upgrades and selection of available versions directly from the control panel. Additionally, it offers advanced configuration options, including PLL management, enabling precise synchronization of the digitizer's internal clock. Access is available via USB or Ethernet, with credential-based authentication ensuring different authorization levels for administrators and standard users.



www.caen.it/products/caen-felib-library/

CAEN FELib Library

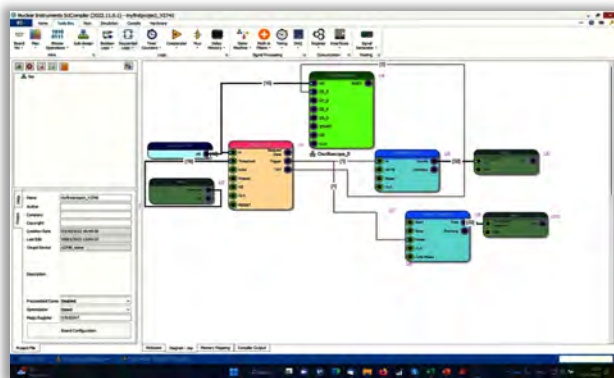
- Set of functions designed for controlling and utilizing Digitizers 2.0 (2740, 2745, 2730, and 2751 family).
- Available in C/C++ and Python for both Windows and Linux operating systems.
- Distributed as an open-source software under the GNU Lesser General Public License 3.
- Provides multithread support for enhanced performance.



CAEN Waveform digitizers x27xx introduce a new way of accessing the firmware parameters, providing an abstraction of the registers in the form of library parameters that are much easier to understand and use.

This new way is designed to simplify the life of those users who need to build their own DAQ system and software and access the digitizer firmware parameters. **CAEN FELib** can be used to control and acquire data from the new generation of CAEN digitizers. This library is only an interface and does not include support for any digitizer family. To use a digitizer, the user must also install the corresponding CAEN Digx library.

FELib is compatible for Windows® and Linux® (x86 and Arm) platforms.



www.caen.it/products/sci-compiler/

Sci-Compiler

- Block-diagram-based programming tool for CAEN Open FPGA boards
- Designed to make FPGA access easier, even for non-expert programmers
- Offers 100+ advanced signal processing blocks for Physics and Nuclear Engineering
- Provides Remote Customization Service for compilation and simula-

Sci-Compiler, short for Scientific Compiler, is a graphical software tool designed to simplify and expedite the implementation of firmware in physics applications for open FPGA CAEN boards. By creating a block diagram, the software can automatically generate firmware that can be directly deployed on compatible hardware. This means that even users without expertise in VHDL/Verilog programming can create their own firmware code. It is a unique tool that generates and compiles FPGA code, downloads it onto the target device, and allows real-time data acquisition on a host computer.

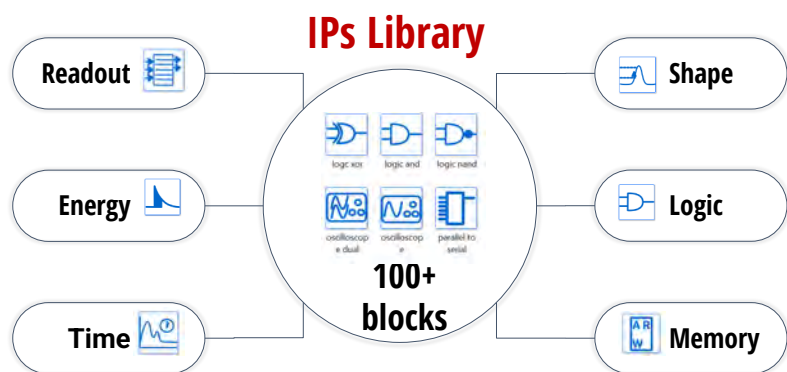
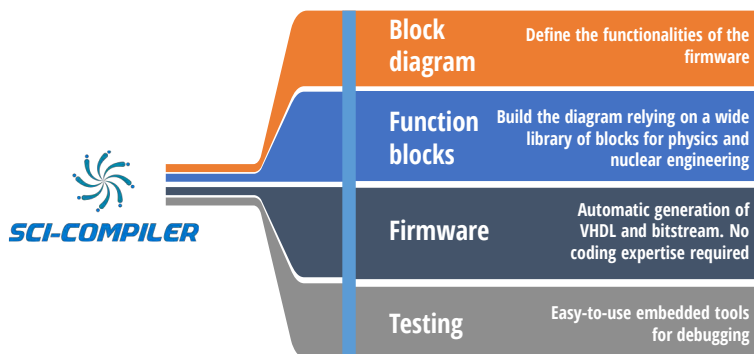
The Sci-Compiler tool includes over 100 virtual blocks that implement complex functions commonly used in physics applications. These functions include waveform recording, logic gates, TDC (Time-to-Digital Converter), spectrum reconstruction, pulse shape discrimination, and more.

Sci-Compiler is currently available for Windows® platforms. 

Sci-Compiler is a software tool that simplifies and accelerates the R&D phase. It allows users to create a diagram by placing and connecting different blocks (e.g. oscilloscope, TDC, MCA, charge integration, etc.) that represent the desired functions. Sci-Compiler then automatically generates and deploys a VHDL code to the FPGA that implements those functions.

This way, even users who are not familiar with VHDL/Verilog can write their own firmware code by focusing on the functional blocks of their application.

In addition, Sci-Compiler provides a Software Development Kit (**Sci-SDK**) for Windows and Linux that is compatible with




the custom firmware for any supported board. The SDK consists of drivers, libraries and example codes in C++ and Python, making it easy to create a custom DAQ software that can run on Windows or Linux.

The Sci-Compiler software enables the development of both purely digital applications, using blocks such as logic gates, scaler, state machine, and analog processing applications, such as custom Multichannel Analyzers, charge integration, Pulse Shape Discrimination, Waveform recording with custom trigger logic, and many more.

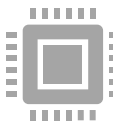
Sci-Compiler is capable of automatically generating VHDL firmware code that implements the functions specified in the block diagram.


The compilation process is made highly accessible through the optional **Remote Customization Service**, which allows you to generate an operating firmware even if you don't have a complete FPGA compiler software installed on your local PC. This greatly simplifies the software setup. By utilizing the Remote Customization Service, Sci-Compiler handles the addition of hard-coded components to your project that remain consistent in each firmware, and generates the final firmware file. This file is then ready to be deployed onboard. CAEN provides the Remote Customization Service and Yearly Upgrade Plan to deliver a high-quality software product with the necessary flexibility to meet the users' requirements.



Sci-Compiler license

Runtime license





Remote Customization / Upgrade

A User Account-Based License allows access to Sci-Compiler through **individual user accounts**. Each user must have a unique account to use the software, and the license is tied to the user's credentials rather than a physical device or hardware key.

Using a single Sci-Compiler license, it is possible to compile and deploy firmware for multiple compatible boards that have been activated through a **runtime license**. A different runtime license is needed for each board.

Remote customization service allows to generate the firmware code with minimal local resources

Stay up-to-date with the newest Sci-Compiler features by subscribing the **yearly upgrade service**



www.sci-compiler.com

Ordering Option

Description	Code
SW555 - Sci-Compiler PRO License	WSW555PROXAA
Sci-Compiler runtime license for Digitizer 2.0 series	WSW555RUNTIME
1 year remote customization service + upgrade for Sci-Compiler	WSW555RCSXAAA
5 years remote customization service + upgrade for Sci-Compiler	WSW555RCSX5YA

Technical Specifications

General Features

Analog Inputs		
Number of Channels	16	Single-ended
Impedance	50 Ω	
Connector	MCX	
Full scale range (FSR)	$2 V_{pp} \div 0.2 V_{pp}$	
Individual Offset adjustable in the range	$\pm 1 V$	
Bandwidth (-3 dB)	500 MHz	
Gain	$\times 1 \div \times 10$ software programmable In steps of 1 dB independently on each channel	

Digital Conversion / System Performance		
Resolution	14 bits	
Sampling Rate	1 GS/s (simultaneously on each input)	
ENOB (Typ)	10.0 (@50MHz, -3dB, Gain $\times 1$)	
RMS (Typ)	3.2 LSB RMS (@Gain $\times 1$)	

Digital I/O and Analog Output		
CLK-IN	Two differential pairs: - CLK: reference clock signal - SYNC: synchronization signal (start/stop, T0, etc.)	AC-coupled LVDS, ECL, PECL, LVPECL, CML Zdiff = 100 Ω 2.54mm 4-pin AMPMODU Mod II male connector
CLK-OUT	Same functionalities as CLK-IN Daisy chainable in multi-board synchronization	LVDS 2.54mm 4-pin AMPMODU Mod II male connector
TRG-IN/TRG-OUT/GPIO/S-IN	General-purpose I/Os Software programmable (trigger, gate, veto, busy, etc.) - TRG-IN/S-IN internally terminated with 50 Ω (Zin = 50 Ω) - TRG-OUT requires Rt = 50 Ω - GPIO as Input must be terminated with 50 Ω - GPIO as TTL Output requires Rt = 50 Ω - GPIO as NIM Output requires Rt = 50 Ω or 25 Ω	Single-ended TTL/NIM (SW programmable) LEMO 00 male connector
LVDS I/O	16 differential pairs Software programmable I/O (individual self-trigger outputs, trigger validations, Veto, Busy, Start, Stop, Pattern Input, etc.)	LVDS Zdiff = 100 Ω (when set as inputs) 2.54mm 34-pin AMPMODU Mod II male connector
DAC OUT	DAC output for signal inspection, pulse generation, majority level 14-bit Digital-to-Analog Converter (DAC) 125 MS/s Update Rate	$\pm 1 V$ @ 50 Ω load $\pm 2 V$ @ hi-Z load Output Range LEMO 00 connector

Acquisition Memory

5 GB total DDR4 memory size (83.886 MS/ch) divisible in multiple buffers
Maximum record length: ~ 84 ms @ 1 GS/s (2-event-buffer configuration)¹

¹ Value referred to the Scope firmware

Communication Interfaces

1 GbE	Copper RJ45 or optical LC connector on SFP+ transceiver TCP protocol	Transfer rate: 110 MB/s
10 GbE (Available on Request)	Copper RJ45 or LC optical connector on SFP+ transceiver UDP protocol	Transfer rate: 850 MB/s
USB 3.0	USB-C type connector USB 3.1 GEN1 protocol	Transfer rate: 280 MB/s

Technical Specifications (continued)

Acquisition and Synchronization

Acquisition Modes		
Triggered	All channels fire simultaneously upon a global trigger generated by the Central Trigger Logic receiving the local trigger sources: Local (self-trigger), External, Software	
Streaming readout	Each channel autonomously identifies the ROI. The channel' self-trigger is used to start the acquisition and capture events independently of the other channels.	
Correlated	Coincidence/anticoincidence logic validates the event acquisition upon the combination of the local trigger with other local triggers and/or external I/Os	
Trigger Sources		
Software	By register writing	
External	Upon the leading edge of TRG-IN signal (TTL/NIM)	
Local (self-trigger)	Upon the channel discriminator with programmable threshold	
Trigger Time Stamp	Scope Firmware	DPP firmware
Resolution:	8 ns coarse time stamp,	1 ns coarse timestamp 1 ps fine time stamp
Counter range:	48 bits	48 bits
Full-scale range:	~625 h	78 h
Synchronization		
Clock Propagation	Typical 62.5MHz frequency distributed by daisy chain either through CLK-IN/CLK-OUT, or by fan-out to CLK-IN, or by P0 backplane (customization available on request) Custom frequencies can be supported	
Acquisition Start/Stop	Daisy chain or fan-out propagation through CLK-IN/CLK-OUT or NIM/TTL, LVDS I/O	
Data Sync	Busy/Veto logic on LVDS I/Os or NIM/TTL I/Os for event building synchronization	
Trigger Time Stamp Reset	Software from START run command or Hardware from S-IN/GPIO input (Scope Firmware only)	
Trigger Distribution	TRG-IN/TRG-OUT NIM/TTL LEMO I/Os (common trigger) or LVDS I/Os (common or individual trigger)	

Firmware and FPGA

FPGA	
Device	Xilinx Zynq UltraScale+ Multiprocessor System-on-Chip mod. XCZU19EG Processing System based on Quad-core Arm with 2GB DDR4 memory @2400 MT/s (Linux OS onboard) Programmable logic with more than 1100K system logic cells and 80Mbit memory
CAEN Firmware	Developed by CAEN, stored in the on-board FLASH memory, and live rebootable by Web Interface
DPP Firmware	Firmware implementing Digital Pulse Processing algorithms: DPP-PSD: Pulse Shape Discrimination, Charge Integration, CFD (Constant fraction Discrimination) for fine time-stamp
Scope Firmware	Firmware for the waveform recording
Upgrades	CAEN firmware can be uploaded via Web Interface or via CAEN ToolBox (scope and DPP firmware, and their updates)
User Firmware (Open FPGA)	
SCI-Compiler	User Firmware Generator and Compiler Graphical Tool for CAEN Programmable Boards
Scope Personalization	Customizable features of the Scope firmware: - Common trigger - Simultaneous waveform recording on 32 channels management - Trigger logic - Wave processing
DPP Personalization	Customizable features of the DPP firmware: - Individual trigger and channel acquisition management - DPP algorithm - Trigger logic - Event data information

Technical Specifications (continued)

Software

Readout Software

CoMPASS	spectroscopy software (CAEN DPP firmware only) for Windows® and Linux®
WaveDump2	(CAEN scope firmware only) for Windows® and Linux®
SCI-Compiler	(Open FPGA): Automatic generation of drivers (USB, ethernet), libraries, and demo software for Windows®

Web Interface

Firmware management (e.g. upgrades and on-the-fly selection of the firmware to run), board information, PLL and Ethernet configuration, board status monitoring, DPP license management

Utilities

CAEN Toolbox Multi-Functional Software Suite for the Upgrade of Front-end Boards, Bridges and Power Supplies

SDK

High-level software library for digitizers, CAEN FELib, available in C and Python with support for Windows, Linux and ARM architectures. The library includes multiple demonstration applications. Source code available on GitHub for further development and integration

Mechanical

	VX2751	DT2751	
Form Factor	1-unit wide VME64X	Desktop	Desktop / Rack
Weight	750 g	3120 g	3170 g
Dimension	6U x 160 mm	338 W x 100 H x 295 L mm ³ (including connectors)	19" rack mount

Environmental

Environmental	Indoor use
Operating Temperature	0°C ÷ +40°C
Storage Temperature	-10°C ÷ +60°C
Operating Humidity	10% ÷ 90% RH non condensing
Storage Humidity	5% ÷ 90% RH non condensing
Pollution Degree	2
Overvoltage Category	II
EMC Environment	Commercial and light industrial
IP Degree	Enclosure (desktop models), not for wet location

Regulatory

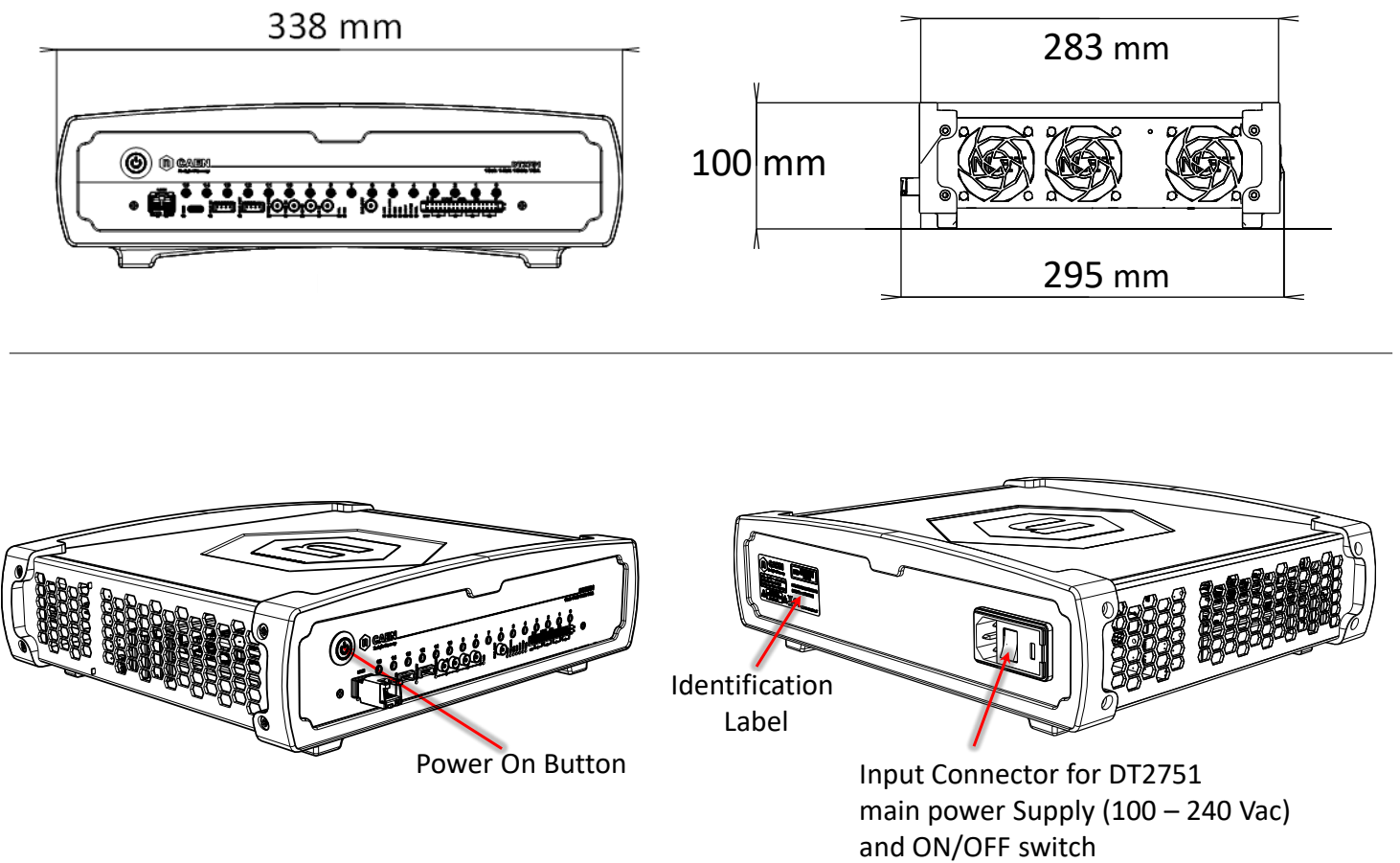
Compliance	EMC: CE 2014/30/EU Electromagnetic compatibility Directive Safety: CE 2014/35/EU Low Voltage Directive
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Power Requirements

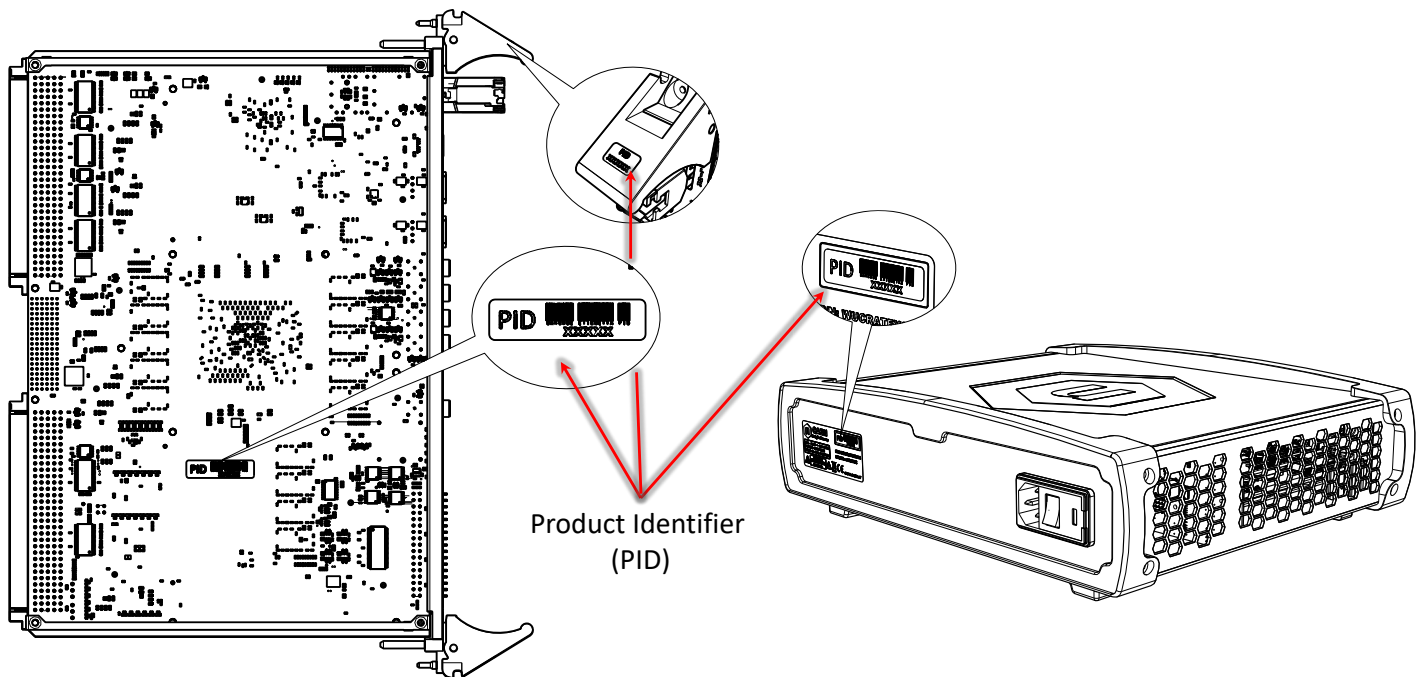
VX2751 (*)	DT2751
+ 12V 0.5 A	Mains-powered (100÷240 Vac, 50÷60 Hz; Max power: 130 W)
+ 5 V 6.4 A	
+ 3.3 V 5.4 A	

(*) These are preliminary values, referred to a 1-GbE Scope firmware, that could vary depending on the firmware type

DT2751 Mechanical Dimension

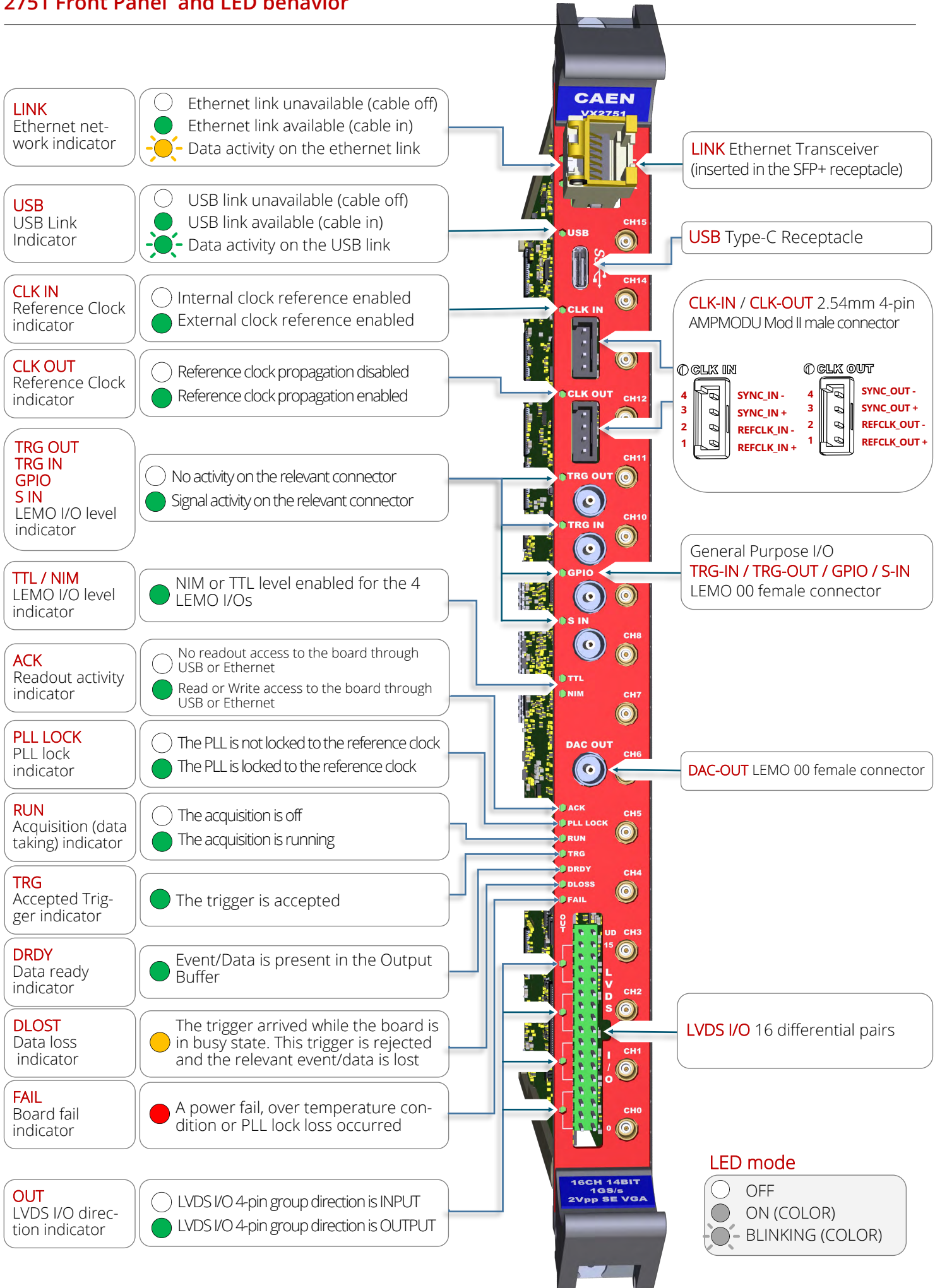


2751 PID (Product Identifier)

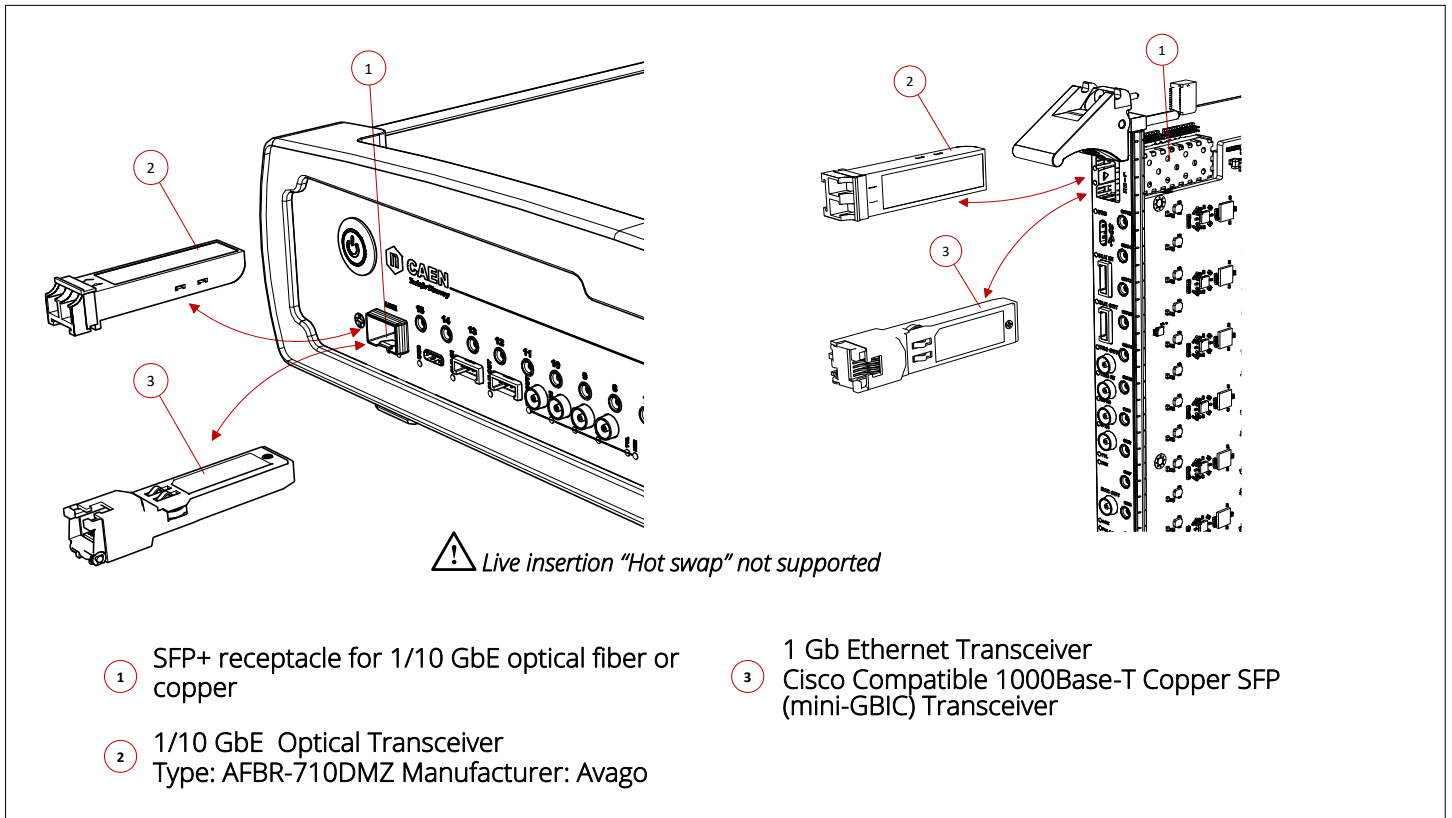


PID (Product Identifier) is a unique incremental number greater than 10000 assigned to each CAEN product. It can be found on a label attached to the product (refer to the figure) and is also stored in an onboard non-volatile memory that can be read via the

2751 Front Panel and LED behavior



2751 SFP+ receptacle for Ethernet interface



Rack Mounting:

Desktop version (DT2751) can be rack mounted using 2 brackets included in the product-kit.



2751 Accessories



A319A Clock & Sync cable assembly

The The A319A is a cable assembly designed for the distribution of Clock and Sync signals in the 27xx Digitizer families. Utilizing the front panel CLK-OUT / CLK-IN daisy chain, this 4-contact cable facilitates the transmission of two differential signals from one digitizer to another, enabling the synchronization of multiple boards.



A319B Clock cable assembly from Digitizer 2.0 series to Digitizers 1.0 series

The A319B is a cable assembly utilized for the distribution of Clock signals between non-homogeneous digitizer series. It connects the 3-contact connector on the Digitizer series 1.0 to the 4-pin connector on the Digitizer series 2.0. Similar to the A319A, this cable enables the transmission of the differential clock signal from one digitizer to another through the front panel CLK-OUT / CLK-IN daisy chain, allowing for the synchronization of multiple boards.

μ-crate

Desktop single-slot VME64X Crate

- Mechanical compatibility: 1-unit VME 6U x 160 mm modules
- Standard power: 10.5 A @+3.3 V, 10 A @+5 V, 2 A @+12 V, 2 A @-12 V
- Fan speed control:
 - Manual adjustment via hardware button (high/low state)
 - Automatic control available only with Digitizer 2.0 series
- Mains-powered (100 ÷ 240 Vac; 50 ÷ 60 Hz; Max power: 130 W)
- Includes a 19" rack mount kit adapter



The μ-Crate is a mains-powered desktop device integrating a low-noise cooling vents system. The desktop form factor can be optionally converted into a 19" rack thanks to the included metal brackets.

The single-slot backplane supports VME64 and VME64X CAEN boards of the Digitizer 1.0, Digitizer 2.0 Families, the V2495 Programmable Logic Unit, as well as other CAEN boards. Only VME Modules (one-unit, 6U x 160mm) with direct connection on the front panel (via USB-2.0/3.0, CONET optical link or 1/10 GbE) and/or not requiring VME bus control (VME protocols not supported) are compatible with the μ-Crate.



The images above show the μ-Crate product with a Digitizer from the 2.0 Family (VX2740) installed, as well as the two possible configurations: Desktop and rack-mountable (using the two brackets included in the sales kit).

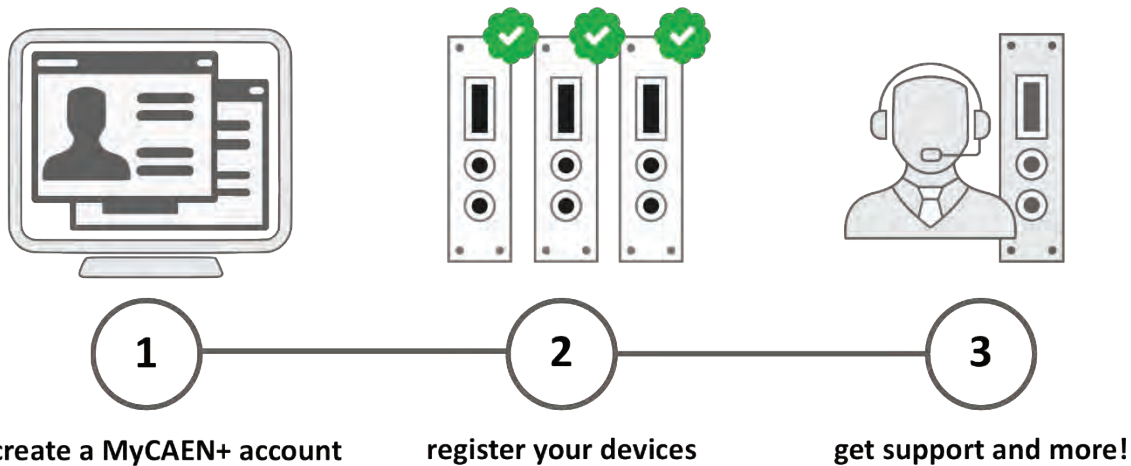
Ordering Option

Description	Code
VX2751 - 16ch 14bit 1GS/s 2Vpp SE VGA Digitizer	WVX2751XAAAA
DT2751 - 16ch 14bit 1GS/s 2Vpp SE VGA Digitizer	WDT2751XAAAA
Accessories	
A319A - Clock & Sync Cable for Digitizers Series 2.0 interconnection (L=20cm)	WA372MXAAAAA
A319B - Clock Cable for Digitizer Series 1.0 to 2.0 interconnection (L=20cm)	WA319BXAAAAA
μ-Crate - Desktop single-slot VME64X Crate	WUCRATEX001A

Register your device

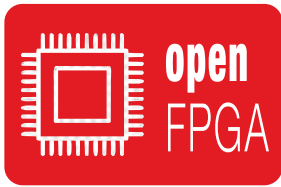
Register your device to your **MyCAEN+** account and get access to our customer services, such as notification for new firmware or software upgrade, tracking service procedures or open a ticket for assistance. **MyCAEN+** accounts have a dedicated support service for their registered products. A set of basic information can be shared with the operator, speeding up the troubleshooting process and improving the efficiency of the support interactions.

MyCAEN+ dashboard is designed to offer you a direct access to all our after sales services. Registration is totally free, to create an account go to <https://www.caen.it/become-mycaenplus-user> and fill the registration form with your data.



<https://www.caen.it/become-mycaenplus-user/>

2751 Digitizer Family



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